OR Firm or LSI LOGIC CORPORATION Individual Name 1621 Barber Lane., MS: D-106 Address **Patent Law Department** Address **MILPITAS** State CA ZIP 95035 City Country **USA** 408-433-7460 408-433-7475 Fax Telephone I am the: Applicant Assignee of Record of the entire intere Certificate under 37 CFR 3.73(b) is enclose SIGNATURE of Applicant or Assignee of Record SANDEEP JAGGI. Name Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp. Signature

Date

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Application Number	09/896,363 June-28, 2001		
Filing Date			
First Named Inventor	Hong-Qiang Lu		
Group Art Unit	2823		
Examiner Name	Fernando L. Toledo		
Attorney Docket Number	LSI1P167 / 01-206		

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hong-Qiang Lu et. al.

Serial No .:

09/896,363

Filed:

June 28, 2001

.<u>For :</u>

Dual Layer Barrier Film Techniques

To Prevent Resist Poisoning

Group Art Un

2823

Examiner:

Toledo, Fernando L.

Atty Docket:

LSI1P167 / 01-206

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

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CERTIFICATION UNDER 37 C.F.R. 3.73(b)

Commissioner for Patents P. O. Box 1450 Aiexandria, VA 22313-1450

Sir:

LSI Logic Coporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, inder Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 (408) 433-7475

Date: 8/10/04

Respectfully submitted,

Sandeep Jaggi Chief IP Counsel

Corporate Assistant Secretary LSI LOGIC CORPORATION